

Roll No.

Subject Code—8049

P.G.D.C.A./M.C.A. EXAMINATION

(Second Semester)

(MCA 3 Years)

(Main/Re-appear Batch 2009 Onwards)

MS-07

**COMPUTER ORGANISATION AND
ARCHITECTURE**

Time : 3 Hours

Maximum Marks : 70

Section A

Note : Attempt any *Seven* questions. **7×5=35**

1. (a) What is Computer Architecture ?
(b) Explain Common Bus System.
(c) Explain fetch, recode and execute cycle with timing diagram.

- (d) What is Control Address Register ?
- (e) What is Sequencer ?
- (f) What is Micro Code ?
- (g) Explain Reverse Polish Notation.
- (h) What is Delay Load and Delayed Branch is RISC pipeline ?
- (i) Explain Daisy-chain priority interrupt.
- (j) Explain Page Replacement.

Section B

Note : Attempt all the questions.

2. Design Accumulator logic with suitable circuit diagram. 12

Or

What is Microprogram ? Explain Micro instruction format and micro operation.

3. Explain different types of Addressing Modes with the help of example. 12

Or

Explain vector processor its operation. Also explain the matrix multiplication using Vector Processing.

4. Explain Isolated I/O interface and Memory Mapped I/O interface. Also explain handshaking in Asynchronous Data Transfer.

11

Or

Explain Memory Hierarchy. Explain Auxiliary Memory in detail.